IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Joseph R. Little

Serial No.: 09/542,782

Filed: April 4, 2000

For: APPARATUS AND METHOD FOR

FEATURE EDGE DETECTION IN SEMICONDUCTOR PROCESSING

Confirmation No.: 6869

Examiner: S. Yam

Group Art Unit: 2878

Attorney Docket No.: 2269-4298US

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Notice of Allowance Mailed:

March 22, 2007

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FEE ADDRESSEE FOR RECEIPT OF PTO NOTICES RELATING TO MAINTENANCE FEES

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

1. This letter is to specify that the FEE ADDRESSEE for this patent is:

MICRON TECHNOLOGY, INC.

Mail Stop 525 8000 South Federal Way Boise, Idaho 83707-0006

- 2. The Customer Number for the Fee Addressee is **26809**.
- 3. Any prior FEE ADDRESSEE for this patent is hereby revoked.
- 4. It is certified that the person whose signature appears below has the authority to change the FEE ADDRESSEE for this patent.

Respectfully submitted,

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